EE 307 : Computer Architecture

The course introduces the architecture of digital systems, with an emphasis on the structural principles common to a wide range of computer technologies. Multilevel implementation strategies, the definition of new primitives (e.g., gates, instructions, procedures, and processes) and their mechanization using lower-level elements, the organization and operation of digital computers and the hardware/software interface are addressed.

Credits 3
Lab Hours 0
Lecture Hours 3
Tutoring Hours 0
Prerequisites
SE 100, EE 210 (or SE 223)
Corequisites
None